FIG. 1

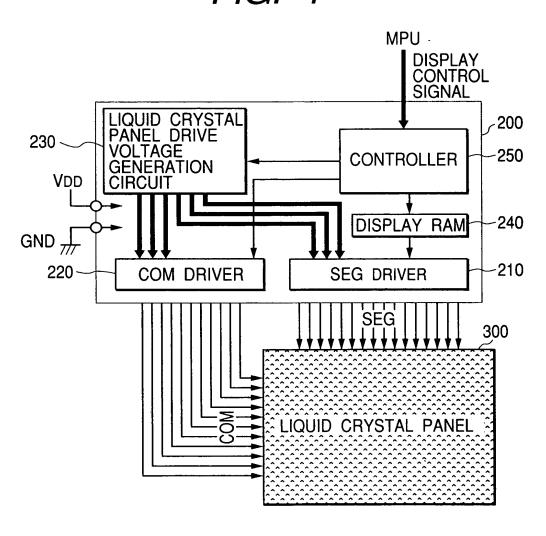


FIG. 2

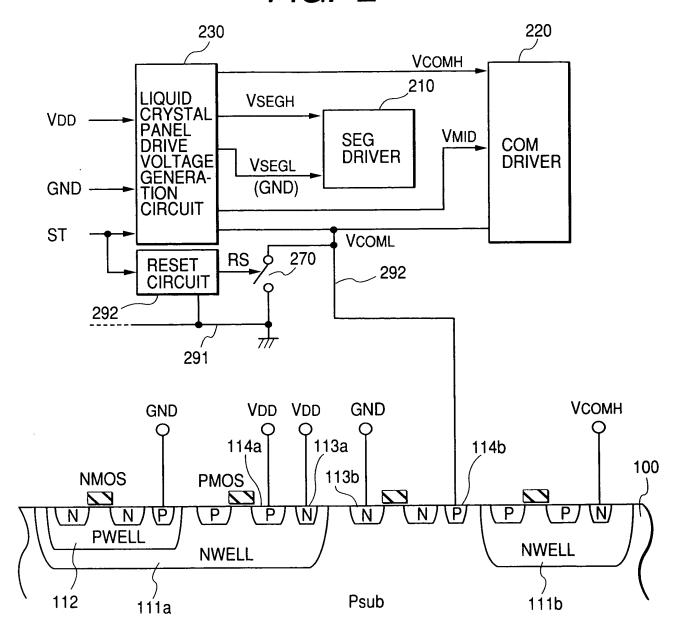


FIG. 3(A)

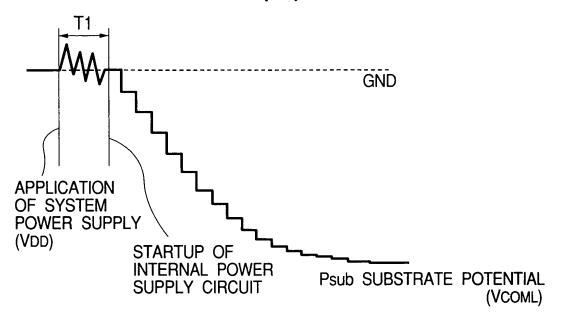


FIG. 3(B)

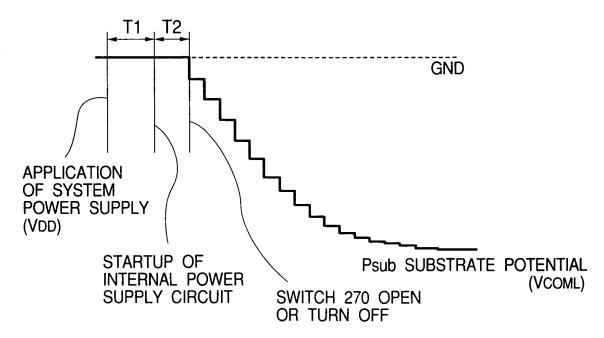
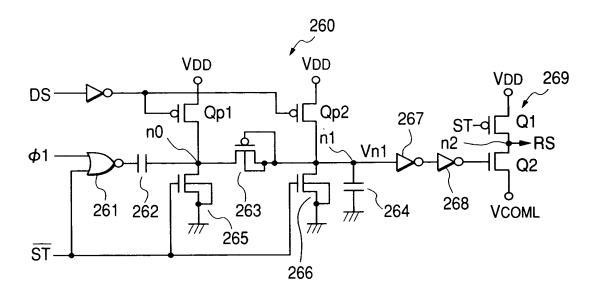


FIG. 4



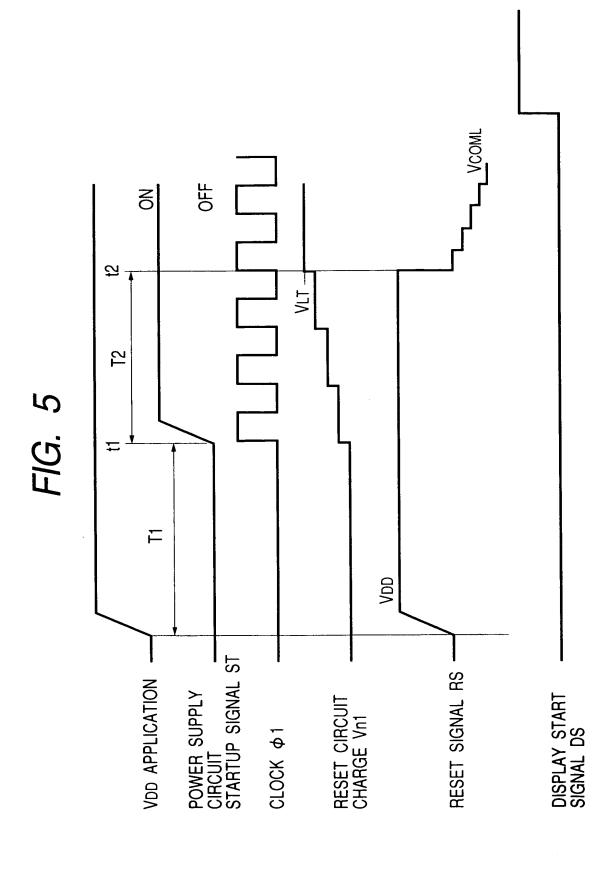
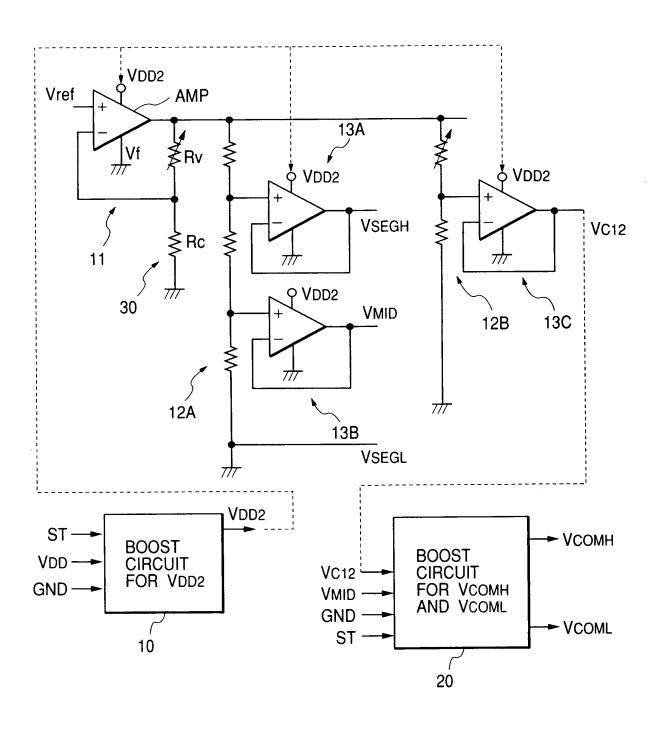


FIG. 6



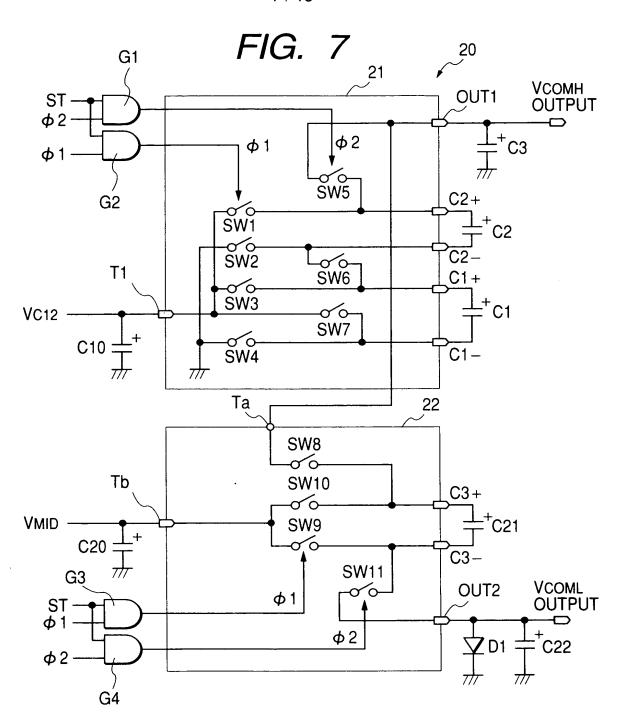


FIG. 8

FIG. 9(A)

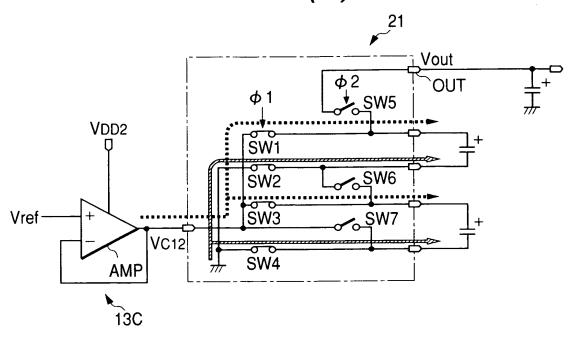


FIG. 9(B)

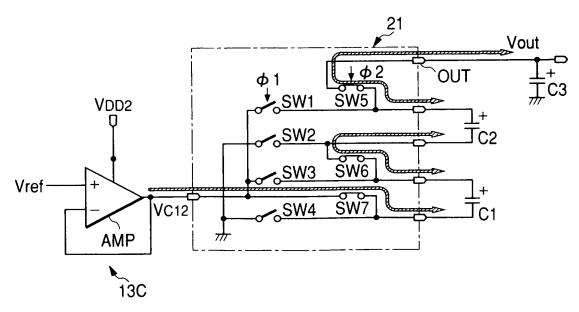


FIG. 10

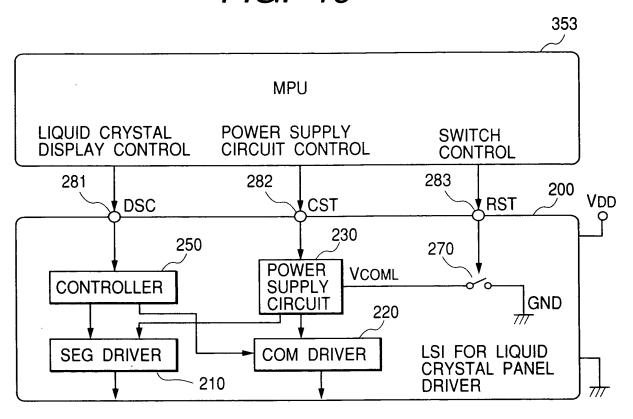
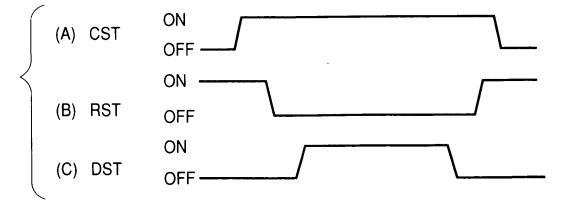


FIG. 11



230 LIQUID CRYSTAL PANEI CONTROLLER DRIVER COMMOR DRIVER 200 250 240 CONTROLLER RAM INTERFACE SEG DRIVER DISPLY 210, HIGH FREQUENCY INTERFACE BASE BAND ASIC MPU DSP 350~1 351 352 -353 -INTERFACE MEMORY 330 360 GND VDD

FIG. 13

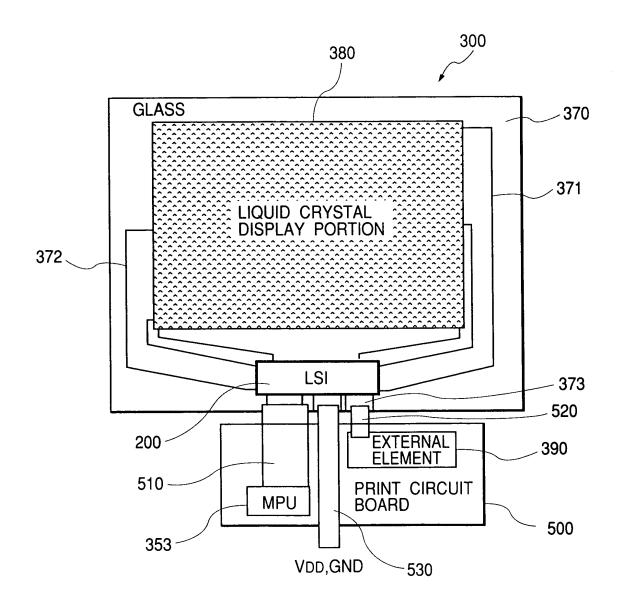


FIG. 14(A)

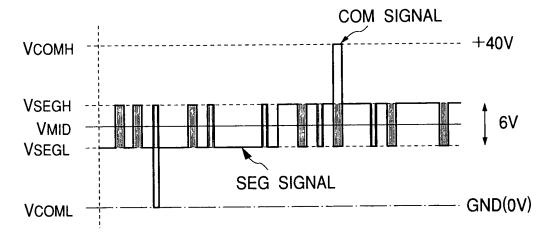
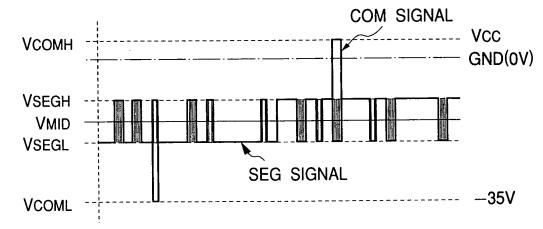


FIG. 14(B)



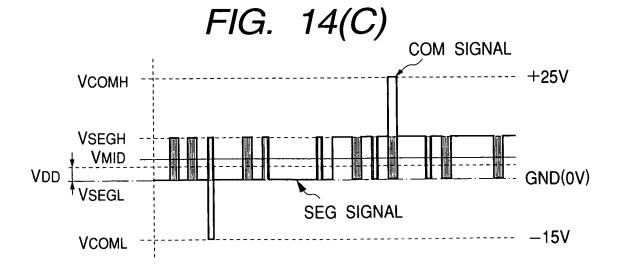


FIG. 15

